Proc. of the 8th International Conference NEET 2013, Zakopane, Poland, June 18-21, 2013

Surface Properties of Me/Si Structures Prepared by Means of Self-Ion Assisted Deposition

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In this paper a composite structure, topography, wettability and nanohardness of a (100) Si surface modified by means of ion-assisted deposition of metal (Me) coatings in conditions of a self-irradiation are discussed.

DOI: 10.12693/APhysPolA.125.1306

PACS: 61.72.uf, 82.80.-d, 62.20.Qp

1. Introduction

The deposition of thin metal films on silicon wafers has both scientific and practical interest. It allows to create insulating or conductive layers, lyophilic or lyophobic surface in different fluid products [1, 2]. Xe ion implantation into silicon is widely used to control damage of its structure [3, 4]. It is remained little knowledge about processes of mass transfer in the silicon coating applied to self-ion assisted deposition (SIAD), and the influence of pre-implantation of xenon on diffusion processes in silicon during the metal coating deposition. In order to control the properties of coatings on the substrate surface it is necessary for diagnostic coating/substrate systems. In this paper a composite structure, topography, wettability, and nanohardness of a surface (100) Si modified by means of ion-assisted deposition of metal (Ti, Co) coatings in conditions of a self-irradiation are discussed.

2. Experimental section

The system employed for the coating deposition has been described in detail elsewhere [5] and consists, essentially, of a vacuum chamber to which a resonance vacuum arc source (RVAS) with metal (titan or cobalt) electrodes to produce a mixture of Me atoms and Me⁺ ions attached is. Substrate (silicon wafers) was floated to a negative potential with respect to the source of 7 kV to accelerate the ion species. The SIAD system was pumped by a conventional diffusion pump and attained during film deposition a base pressure of 10^{-2} Pa.

To investigate atom mixing processes at an interface region of a coating/substrate it is useful to know a position of initial surface of substrate (PISS). For this purpose we implanted Xe⁺ ions in the part of substrates. The energy of ions was 10 or 40 keV, doses — in range 1×10^{14} – 2.7×10^{15} Xe/cm².

The composition of the coating/substrate structures and the damage of irradiated substrate were investigated using the Rutherford backscattering/channeling spectroscopy (RBS/RCS). The energy of the He⁺ ions was 2.0 MeV and the energy resolution of analyzing system was 25 keV. The experimental RBS data for concentration against depth were compared with data from the RUMP code simulation [6]. The average value of the projected range of elements in the matrix were calculated using a computer program TRIM-89 [7].

Roughness parameters of virgin silicon substrate and substrate with Me coatings were measured using atomic force microscope NT-206 and corresponding software.

The wettability of the surface of the coating was determined by the values of the contact angle, which is measured by sitting drop method. The droplet volume was 9.3 μ l.

The nanoindentation data from coated systems were used for calculation of the hardness and elastic modulus using load and displacement sensing indentation experiments. During the measurements, the curve of loading and unloading were obtained, which were processed by the method of Oliver-Pharr [8].

The thickness of the deposited coating has a value in the range 180-200 nm.

3. Results and discussion

Figure 1 shows the profiles of the depth distribution structure components obtained during the application of cobalt films on Si, without the xenon irradiation (a) and with it (b).

It is found that the coating composition comprises metal atoms, hydrogen, carbon, oxygen, and silicon. The appearance in the studied coatings O, C, H we associate with the deposition on the surface of the coating during its growth together with the atoms Co, hydrocarbon fraction and O from the residual vacuum in the target chamber, steam-pumped by the diffusion oil pump. Analyzing the results, Fig. 1a and b, let us note that the carbon and oxygen atoms penetrate into the silicon substrate by about 30 nm deeper if coating/substrate was pretreated by xenon ions. Cobalt atoms penetrate into the silicon to a depth of many more times deeper than the projected range calculated by the program TRIM. In the Si wafer pre-implanted with Xe⁺ ions penetration depth of the cobalt atoms increases by ≈ 120 nm. The concentration of cobalt atoms on a comparable depth increases by a factor of 1.5. This effect can be explained by radiation-enhanced diffusion of Me atoms during deposi-

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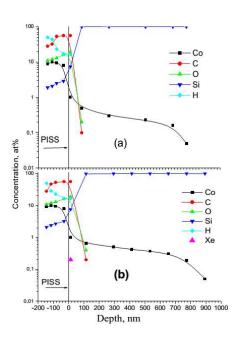


Fig. 1. Relative content of species in SIAD Co thin film on silicon: (a) only SIAD process, (b) SIAD process after Xe irradiation.

tion of coatings on Xe-radiated silicon. Comparison of align and random spectra from the virgin and Me coated (100) Si is done to determine the position of the metal atoms in the crystal lattice of silicon at different depths using known technique [9].

The character of the distribution of the cobalt atoms in silicon which are substitutions, curve Co_{sub}, and interstitials, curve Co_{int}, suggests a plausible mechanism of Me atoms diffusion. In the silicon near PISS (≈ 10 nm) the greatest damage of the crystal lattice is generated, and there is observed a sharp decrease in the concentration of cobalt atoms. This is explained due to the fact that with increase of number of interstitial silicon atoms there is decrease of the possible migration paths of Co atoms along the silicon interstitials in the silicon lattice. At greater depths the proportion of cobalt atoms being in the crystal lattice increases, reaching a value of 40% at about 780 nm. Analysis of the distribution of the cobalt atoms in silicon, previously irradiated Xe⁺ ions with the energy of 10 keV and doses value of 3×10^{14} Xe/cm², Fig. 2b, suggests that the mechanism of Co diffusion using substitution is essential. Pre-irradiation of silicon by xenon ions leads to greater damage of the Si structure and to increase of the cobalt atoms in substitute positions. This effect has dose dependence. Irradiation of silicon with Xe⁺ ions leads to decrease of proportion of Co interstitials from 86% to 30% while Xe⁺ ions dose increases from $1 \times 10^{14} \text{ Xe/cm}^2$ to $2.7 \times 10^{15} \text{ Xe/cm}^2$ at a depth of penetration ≈ 90 nm.

Let us note that the physical processes of Ti/substrate structures formation is qualitatively comparable to the processes that occur during the deposition of cobalt on silicon surfaces.

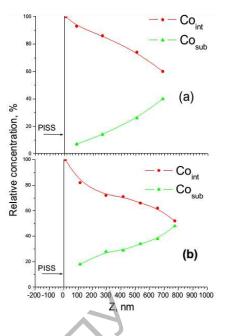


Fig. 2. The distribution of the cobalt atoms in silicon of Co/Si structures, formed by: (a) only SIAD process, (b) SIAD process with the pre-implanted Xe⁺, $E_{\rm xe} = 10$ keV, $D = 3 \times 10^{14}$ cm⁻².

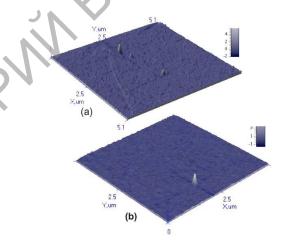


Fig. 3. Three-dimensional AFM surface morphology of silicon with Ti coating: (a) only SIAD process, (b) SIAD process after Xe irradiation.

In the study of topography of systems coating/substrate and the pure silicon site $5 \times 5 \ \mu m^2$ were chosen. According to 3D images of the surface topography of titanium coating in Fig. 3 one can conclude that the high quality surface on silicon is generated.

Figure 4 shows photographs of water droplets on the surface of the Ti coating.

Parameters of the surfaces of the deposited coatings are shown in Table.

As follows from Table xenon irradiation does not cause significant changes of distilled water wettability. Generally, there are the changes of the roughness parameters within nanometer range in our case. That is why it can TABLE

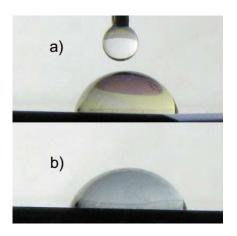


Fig. 4. Pictures of water droplets on the surface of the Ti coating on the silicon: (a) only SIAD process, (b) SIAD process after Xe irradiation.

Parameters of surfaces of the Ti and Co coatings deposited on silicon.

Parameter	Ti coating		Co coating	
	$\begin{array}{c} {\rm without} \\ {\rm irradiation} \end{array}$	with irradiation	without irradiation	with irradiation
contact angle [deg]	77.8	78.4	60.5	59.3
$R_{\rm ave} [\rm nm]$	0.123	0.158	0.114	0.113

be concluded that SIAD process of Me thin films on silicon is appropriated for microelectronics.

The results of measuring of nanohardness of the formed Me/Si structures are shown in Fig. 5.

The hardness of the surface of Ti coatings (thickness of 50 nm layer) increases 4-fold and we observe 4.5-fold increase when applied to cobalt. With increase of the depth of the indentation integral nanohardness of the structures coating/substrate approaches to the nanohardness of silicon.

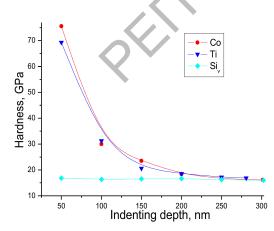


Fig. 5. The dependence of the surface nanohardness of the Me/Si structures on the depth of the indentation.

4. Conclusion

We have shown that Me coatings produced by means of SIAD have a multi-component structure. Thin films deposited on silicon consist of Me, C, O, Si, and H. The high content of C, O, and H arises, we suppose, because of the poor vacuum used, and it allows capture of oxyhydrocarbons onto the growing film and accumulation of them under irradiation with the accelerated Me⁺ ions. Silicon appears in the coatings due to an out--diffusion process from the bulk. Deep penetration of Me atoms into silicon and appearance of Si atoms in coatings are attributed to radiation-enhanced diffusion processes during self-ion assisted deposition of thin cobalt and titanium based films upon silicon. Implantation of Xe⁺ ions in substrate before deposition of Me coatings promotes radiation-enhanced diffusion of components in Me/Si structures.

The experimental results confirm the possibility to control the mass transfer in Si introduction to the structure of radiation defects. With the use of scanning probe microscopy high quality and low surface roughness obtained structures Ti/Si and Co/Si, indicating the possibility of surface roughness control systems Me/Si, are used for various functional elements. It is found that pre-irradiation of silicon by xenon ions does not lead to a significant change in the wettability of the surface of the obtained structures. The study of the surface nanohardness of the coatings showed that the SIAD process creates coating hardness which is comparable to the hardness of diamond-like films. Ion-assisted deposition process of Me coatings on silicon may be applied in microelectronics.

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